

High Speed Low Noise LDO

General Description

The RLCP6219 series are highly accurate,low noise,CMOS LDO Voltage Regulators.Offering low output noise, high ripple rejection ratio, low dropout and very fast turn-on times, the RLCP6219 series is ideal for today's cutting edge mobile phone. Internally the RLCP6219 includes a reference voltage source, error amplifiers, driver transistors, current limiters and phase compensators.

The output voltage is set by current trimming. Voltages are selectable in 100mV steps within a range of 1.2V to 5.0V. The RLCP6219 series is also fully compatible with low ESR ceramic capacitors, reducing cost and improving output stability. This high level of output stability is maintained even during frequent load fluctuations, due to the excellent transient response performance and high PSRR achieved across a broad range of frequencies. The CE function allows the output of regulator to be turned off, resulting in greatly reduced power consumption.

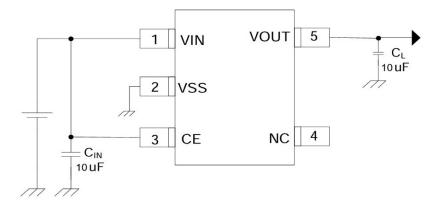
Features

- Low power consumption:40uA (Typ.)
- Low voltage drop: 0.12V@100mA@VOUT=3.3V(Typ.)
- > Standby Mode: 0.1uA
- Low temperature coefficient
- High input voltage (up to 8V)
- Output voltage accuracy: tolerance ±2%
- SOT23-5 SOT89 package

Applications

- Battery-powered equipment
- Communication equipment
- Mobile phones
- Portable games
- Cameras, Video cameras
- Reference voltage sources

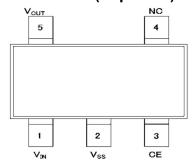
Typical Application Circuit





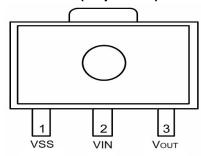
Package and Pin assignment(Top View)

SOT23-5(Top View)



Pin Number	Symbol	Function
1	VIN	Power Input Pin
2	VSS	Ground
3	CE Chip Enable Pin	
4	NC	No Connection
5	VOUT	Output Pin

SOT89(Top View)

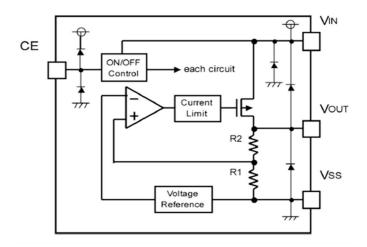


		CO.LTO
Pin Number	Symbol	Function
1	VSS	Ground
2	VIN	Power input Pin
3	Vout	Output Pin

Order information

				VIIN	I OWCI
		2 3	3	Vouт	Out
		IN VOUT		10 17	6,
C	Order informa	tion	CHUM	(HE)	
F	RLCP6219-① ②	345/R6	M. D.	11.	
			_		
	Designator	Symbol	Des	scription	
	Designator ① ②	Symbol Integer	Output Voltage		2.8V)
	12	-	Output Voltage		2.8V)
		Integer	Output Voltage (eg:28, Output 2	2.8V)
	12	Integer ST5	Output Voltage S	eg:28, Output 2	2.8V)

Block Diagram





Absolute Maximum Ratings

Item	Min	Max	Unit
Supply Voltage (1)	-0.3	8.0	V
Operating Temperature Range	-40	+85	C
Storage Temperature Range	-50	+125	°C

Note:

These are stress ratings only. Stresses exceeding the range specified under "Absolute Maximum Ratings" may cause substantial damage to the device. Functional operation of this device at other conditions beyond those listed in the specification is not implied and prolonged exposure to extreme conditions may affect device reliability.

Thermal Information

ltem	Package	Value	Unit	
D D: : (:	SOT23-5	0.25		
Power Dissipation	SOT89	0.5	W	
Thermal Resistance Oja (Junction to Ambient)	SOT23-5	500	2	
(Assume no ambient airflow, no heat sink)	SOT89	200	°C/W	
Note: PD is measured at Ta= 25℃	HUANG EL			



Electrical Characteristics (Ta=25℃)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Output Voltage	Vout	Vin=Vout+1V 1.0mA≤lout≤30mA	Vout x0.98		Vout x1.02	V
Output Current*1	lout	Vin-Vout=1V		350		mA
Line Regulation	∆Vout1/ (ΔVin·Vout)	4.3V≤Vin≤8V lout=10mA	-	0.05	0.2	%V
Load Regulation	ΔVout	Vin=4.3V 1.0mA≤lout≤100mA	-	10	30	mV
Output voltage Temperature Coefficiency	ΔVout/(Ta·Vout)	lout=30mA 0°C≤Ta≤70°C		±100		Ppm/℃
Supply Current	lss1		-01	40	15	uA
Input Voltage	Vin	()	CLE	~ <i>7</i> 20	8	V
PSRR	PSRR	F=1KHz, Vin=4.3Vdc+1Vpp	1	60		dB

Notes

- 1. Please use this IC within the stated absolute maximum ratings. The IC is liable to malfunction should the ratings be exceeded.
- 2. Where wiring impedance is high, operations may become unstable due to noise and/or phase lag depending on output current. Please keep the resistance low between VIN and VSS wiring in particular.
- 3. Please wire the input capacitor (CIN) and the output capacitor (CL) as close to the IC as possible.



Output Voltage Control

The P-channel MOSFET is connected to the VOUT pin, driven by the subsequent output signal. The output voltageat the VOUT pin is controlled and stabilized by a system of negative feedback. The IC's internal circuitry can shut-down by the CE pin's signal

Low ESR Capacitors

With the RLCP6219 series, a stable output voltage is achievable even if used with low ESR capacitors as a phase compensation circuit is built-in. In order to ensure the effectiveness of the phase compensation, we suggest that an output capacitor (CL) is connected as close as possible to the output pin (VOUT) and the GND pin. Please use an output capacitor with a capacitance value of at least 10uF. Also, please connect an input capacitor (CIN) of 10uF between the VIN pin and the GND pin in order to ensure a stable power input. Stable phase compensation may not be ensured if the capacitor runs out capacitance when depending on bias and temperature. In case the capacitor depends on the bias and temperature, please make sure the capacitor can ensure the actual capacitance.

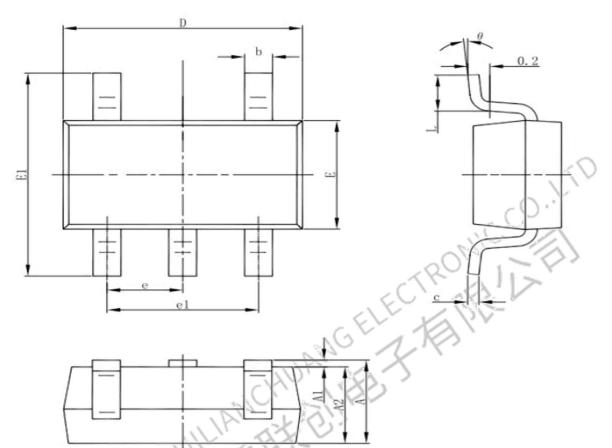
CE Pin

The IC's internal circuitry can be shutdown via the signal from the CE pin with the RLCP6219 series. The operational logic of the IC's CE pin is selectable (please refer to the selection guide). Although the CE pin is equal to an inverter input with CMOS hysteresis, with either the pull-up or pull-down options, the CE pin input current will increase when the IC is in operation. We suggest that you use this IC with either a VIN voltage or a VSS voltage input at the CE pin. If this IC is used with the correct specifications for the CE pin, the operational logic is fixed and the IC will operate normally. However, supply current may increase as a result of through current in the IC's internal circuitry.



Packaging Information

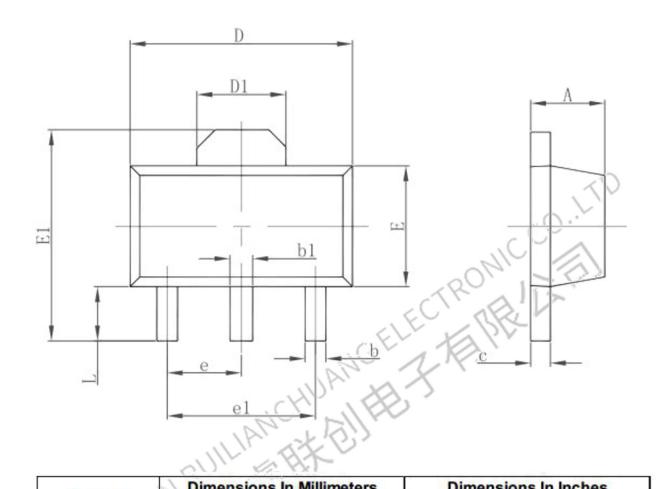
SOT23-5 Outline Dimensions



C b l	Dimensions In	Millimeters	Dimensions	In Inches
Symbol	Min <	Max	Min	Max
A. N	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
-A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
С	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
е	0.950(BSC)		0.037(BSC)
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°



3-pin SOT89 Outline Dimensions



Combal /	Dimensions In Millimeters		Dimensions In Inches	
Symbol	Min.	Max.	Min.	Max.
A	1.400	1.600	0.055	0.063
, b	0.320	0.520	0.013	0.020
5 b1 -4	0.400	0.580	0.016	0.023
c /	0.350	0.440	0.014	0.017
D	4.400	4.600	0.173	0.181
D1	1.550 REF.		0.061 REF.	
E	2.300	2.600	0.091	0.102
E1	3.940	4.250	0.155	0.167
е	1.500 TYP.		0.060	TYP.
e1	3.000 TYP.		0.118	TYP.
L	0.900	1.200	0.035	0.047



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